

[THIN FILM TRANSISTOR ARRAY]

Abstract

A thin film transistor array including a substrate, scan lines, data lines, thin film transistor, pixel electrodes, bottom electrodes and connecting conductive layers. The scan lines and the data lines are disposed over the substrate and is divided to pixel areas by the scan lines and the data lines. Each thin film transistor is disposed in one of the pixel areas and is driven by the scan lines and the data lines correspondingly. Each pixel electrode is disposed in one of the pixel areas and is electrically connected to one of the thin film transistors correspondingly. A portion of each pixel electrode is located above one of the scan lines. Each bottom electrode is disposed between one of the pixel electrodes and one of the scan lines. Each connecting conductive layer is located over and electrically connected between one of the bottom electrodes and one of the scan lines correspondingly.